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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10091234	FILING DATE 03/04/2002	CLASS 367	SUBCLASS 249	GAU 2861	EXAMINER H:IAN TRAN BRINK, S
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****APPLICANTS:** Takaki Kouichi; Morita Shinji;

****CONTINUING DATA VERIFIED:** *None*

**** FOREIGN APPLICATIONS VERIFIED:** *me*
JAPAN 083302/2001 03/22/2001

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO
35 USC 119 conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		02111/LH
Verified and Acknowledged Examiners' initials <i>me</i>		
TITLE : Clock-generating circuit and image-forming apparatus having a function of canceling unevenness of scanning-light amount		

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg. Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner	
		PREPARED FOR ISSUE	
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